IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of: GARVIN

Serial No.: To Be Assigned

Filed: Herewith

Title: METHOD AND SYSTEM FOR ADJUSTING A FREQUENCY RANGE OF A

DELAY CELL OF A VCO

Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT (IDS) UNDER 37 CFR 1.97

Dear Sir:

Applicants submit herewith patents, publications or other information which they are aware, which they believe may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR 1.56.

While this Information Disclosure Statement may be "material" pursuant to 37 CFR 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such.

In accordance with 37 CFR 1.97(g) the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56(a) exists.

The attached form, PTO-1449, provides a listing of patents, publications, or other information as required by 37 CFR 1.98(a)(1).

This Information Disclosure Statement is being submitted without copies. This protocol conforms with 37 CFR 1.98(a)(2)(i), which waives the requirement for submitting a copy of each cited U.S. Patent and each U.S. patent application publication for all U.S. national patent applications filed after June 30, 2003.

Respectfully submitted,

August 13, 2003

By:

Stephen G. Sullivan
Sawyer Law Group LLP

Attorney for Applicant(s)

Registration No. 38,329

FORM PTO-1	449 (Modified)	ATTY. DOCKET NO. RPS920030101US1		SERIAL N	SERIAL NO. To Be Assigned		
APPLICANT'S	ENTS AND PUBLICATION E STATEMENT			To Be Ass			
Page 1 of 2		APPLICANT: GARVIN					
(Use several sh	neets if necessary)	FILING DATE: Herewith	GROUP: To Be Ass	GROUP: To Be Assigned			
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	5 5 5 9 4 7 3 09-2	24-96 A	nderson et al.				
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	"A Low-Noise, 900-MHz V Circuits, Vol. 34, No. 5 Ma		CMOS", Chan-Hong Pa	rk and Beomsu	p Kim; IEEE Jou	rnal of Solid-S	State
EXAMINER	DATE CONSIDERED						
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through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.